	Application No.	Applicant(s)
Notice of Allowability	10/002,925	KUX ET AL.
	Examin r	Art Unit
	Nema O Berezny	2813
Th MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>paper filed 5-26-04.</u>		
2. 🔀 The allowed claim(s) is/are <u>5-20</u> .		
3. The drawings filed on <u>02 November 2001</u> are accepted by the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some* c) None of the: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. PCTEP03834. 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). * Certified copies not received:		
 Attachm nt(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date	6. Interview Summar Paper No./Mail Do 08), 7. Examiner's Amend	ate

DETAILED ACTION

Election/Restrictions

This application is in condition for allowance except for the presence of claims 1 to 4 non-elected without traverse. Accordingly, claims 1-4 been cancelled. Applicant previously requested a rejoinder of non-elected claims upon allowance of the elected claims. However, the non-elected process claims do no contain all of the elements of the elected allowable product claims. Therefore, according to MPEP 821.04, the non-elected process claims will not be rejoined to the allowed product claims.

Allowable Subject Matter

The following is an examiner's statement of reasons for allowance:

The prior art of record does not teach or disclose or make obvious a multi-dimensionally constructed chip stack, comprising inter alia: conductor tracks within chips of said chip stack with feed-through contacts interconnecting the conductor tracks of the different chips to form a continuous electrical signal path; and a determining device operatively connected to a receiving device, wherein said determining device ensures deactivation of security-relevant functional components in the chips upon determination of damage in the chip stack.

Pedersen et al. (5,936,302) discloses a similar device, except the interconnects run outside of the chips instead of internally as claimed, to thereby secure said device (Fig.1).

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Urban et al. (6,434,712) discloses a similar device, except there is no specific disclosure as to how the multi-chip module is constructed (col.11 lines 1-2). In addition, Urban deactivates certain signals when said signals were sent in error or are redundant and unnecessary, and uses the paths again later; there is no provision for deactivation when there are damaged chips (col.6 lines 30-42).

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nema O Berezny whose telephone number is (571) 272-1686. The examiner can normally be reached on M-F 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead, Jr. can be reached on (571) 272-1702. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

NB

CRAIG A. THOMPSON